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February 18, 2004

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: **Applicant(s):** Christopher W. Dix
Assignee: Lattice Semiconductor Corporation
Title: Power Supply Remote Voltage Sensing
Serial No.: 10/728,685 Filed: 12/05/2003
Examiner: Unknown Group Art Unit: Unknown
Docket No.: M-15299 US

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

(1) Return Receipt Postcard;
(2) This Transmittal Letter (in duplicate); and
(3) Submission of formal drawings with two (2) sheets of formal drawings including Figures 1, 2, and 3.

No additional fee is required.
 The fee has been calculated as shown below:

CLAIMS AS AMENDED

<u>Claims Remaining <i>After</i> Amendment</u>		<u>Highest No. Previously Paid For</u>		<u>Present Extra</u>		<u>Rate</u>	<u>Additional Fee</u>
Total Claims	24	Minus	24	=	0	x \$18.00	\$ 0
Independent Claims	4	Minus	4	=	0	x \$86.00	\$ 0
<input type="checkbox"/> Fee of _____ for the first filing of one or more multiple dependent claims per application						\$	
<input type="checkbox"/> Fee for Request for Extension of Time						\$	
<u>Total additional fee for this Amendment:</u>						\$	
<input checked="" type="checkbox"/> Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.							
<input type="checkbox"/> Please charge our Deposit Account No. 50-2257 in the amount of						\$	
<input checked="" type="checkbox"/> Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257							

Total: \$ 0

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 18, 2004.



February 18, 2004
Date of Signature

Respectfully submitted,

Amelia M. Tolson

Greg J. Michelson
Attorney for Applicants
Reg. No. 44 940



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Christopher W. Dix
Assignee: Lattice Semiconductor Corporation
Title: Power Supply Remote Voltage Sensing
Serial No.: 10/728,685 Filing Date: 12/05/2003
Examiner: Unassigned Group Art Unit: Unknown
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Irvine, California
February 18, 2004

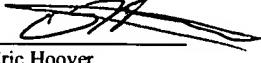
Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit two (2) sheets of formal drawings, consisting of Figures 1, 2, and 3, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on February 18, 2004.


Eric Hoover

February 18, 2004

Respectfully submitted,



Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940

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